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CLAIM AMENDMENTS:

- 1-20. (Cancelled)
21. (Currently amended) An apparatus comprising:
a machine readable storage medium having stored thereon instructions
capable of being executed by a data processing platform, said instructions
being adapted to:
receive a literal source code macroinstruction;
encode said the literal source code macroinstruction into a corresponding
subroutine address without an intermediate translation;
generate an execution stream; and
store the subroutine address.
22. (Currently amended) The apparatus of claim 21, wherein said the
instructions are further adapted to execute a subroutine identified by said
the subroutine address.
23. (Currently amended) The apparatus of claim 22, wherein said the
instructions are further adapted to push an at least one associated argument
onto a stack, said the at least one associated argument adapted to be used
as an input to said the subroutine identified by said the subroutine address.
24. (Currently amended) The apparatus of claim 22, wherein said the
instructions are further adapted to pop an the at least one associated
argument from a stack, said the at least one associated argument adapted
to be used as an input to said the subroutine identified by said the
subroutine address.
25. (Currently amended) The apparatus of claim 22, wherein said the
instructions are further adapted to push a result of the execution of said the
subroutine onto a stack.

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26. (Currently amended) The apparatus of claim 22, wherein said the instructions are further adapted to point to the first item associated with said the subroutine stored in said the execution stream.
27. (Currently amended) The apparatus of claim 21, wherein said the instructions are further adapted to recursively execute a subroutine.
28. (Currently amended) A method comprising:
receiving a source code command input stream comprising a macroinstruction;
encoding said macroinstruction into a corresponding subroutine address without an intermediate translation;
generating an execution stream for storing said the subroutine address and associated arguments; and
executing a subroutine identified by said the subroutine address.
29. (Currently amended) The method of claim 28, and further comprising pushing an argument onto a stack, said the argument representing an input to said subroutine identified by said the subroutine address.
30. (Currently amended) The method of claim 28, and further comprising popping an argument from a stack, said the argument representing an input to said subroutine identified by said the subroutine address.
31. (Currently amended) The method of claim 28, and further comprising pushing a result of the execution of said the subroutine onto a stack.
32. (Currently amended) The method of claim 28, and further comprising pointing to the first item associated with said the subroutine stored in said the execution stream.

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33. (Currently amended) An apparatus comprising:
a machine readable storage medium having stored thereon instructions
capable of being executed by a data processing platform, said instructions
being adapted to:
encode macroinstructions to provide a corresponding executable address
without an intermediate translation.
34. (Currently amended) The apparatus of claim 33, wherein said the
instructions are further adapted to receive the macroinstructions.
35. (Currently amended) The apparatus of claim 33, wherein said the
instructions are further adapted to generate an execution stream.
36. (Currently amended) A method comprising:
translating a source code instruction to generate a subroutine address
without an intermediate translation.
37. (Currently amended) The method of claim 36, wherein translating the
source code instruction includes directly translating the source code.
38. (Canceled) Please cancel Claim 38 without prejudice.
39. (Currently amended) The method of claim 36, further comprising
receiving the a-source code instruction.
40. (Currently amended) The method of claim 36, wherein translating the
source code instruction includes parsing the source code instruction.
41. (Currently amended) The method of claim 36, further comprising
generating an execution stream for storing said the subroutine address.